

APPROVED O.G. FIG. C
BY CLASS SUB J
DRAFTSMAN 438 706

TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
SUBSTRATE DURING GATE STACK ETC
Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

1/14

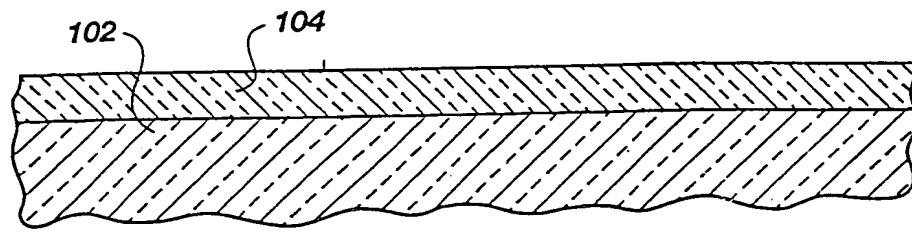


Fig. 1

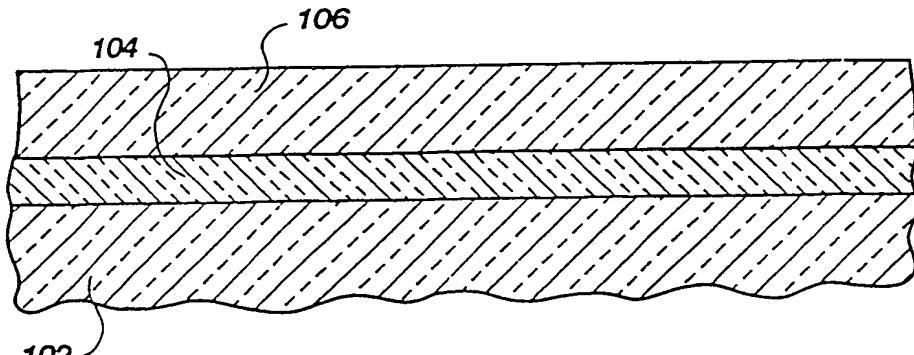


Fig. 2

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TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
SUBSTRATE DURING GATE STACK ETC.

1. PBE
CLASS 377.02
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438 706

Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US



2/14

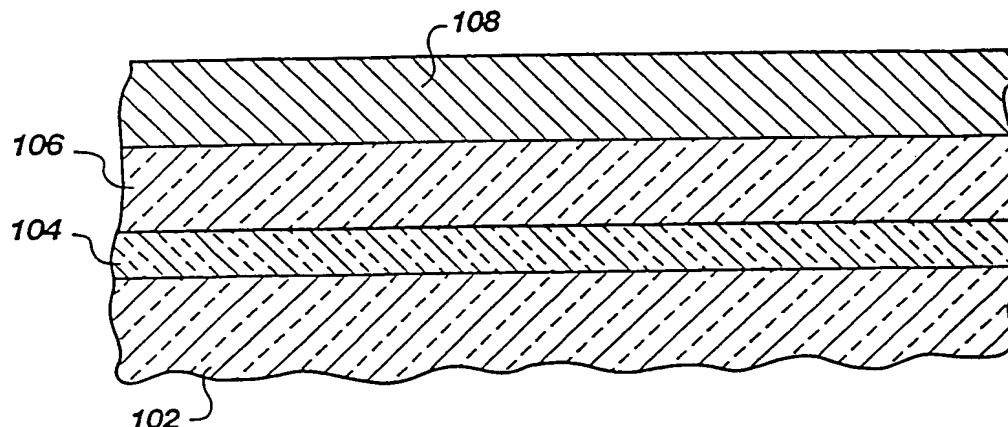


Fig. 3

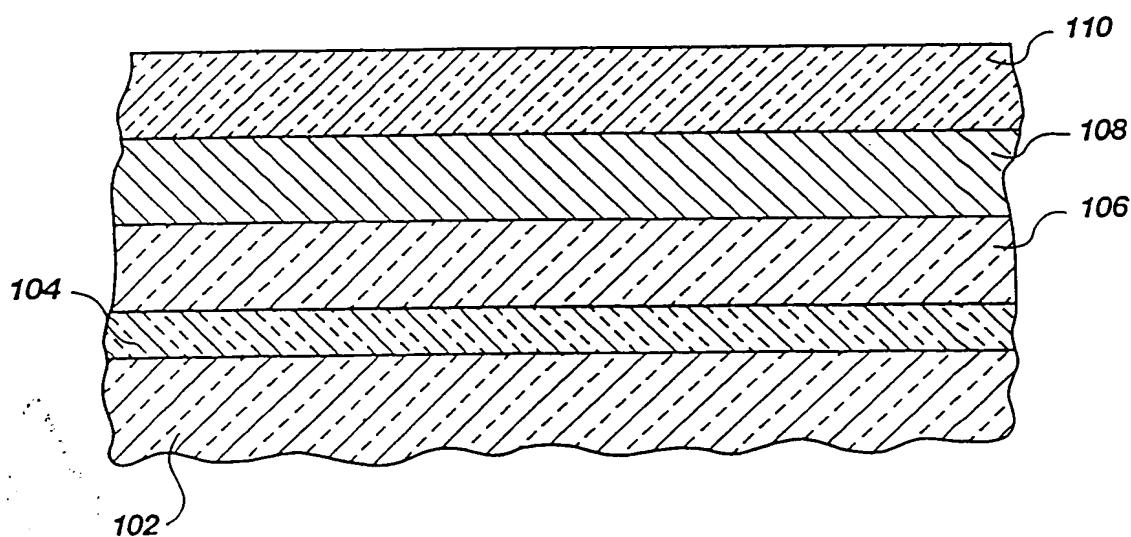


Fig. 4

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O.G. FIG. 6
CLASS 438 SUBCLASS 708

TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
SUBSTRATE DURING GATE STACK FORMATION

Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

3/14

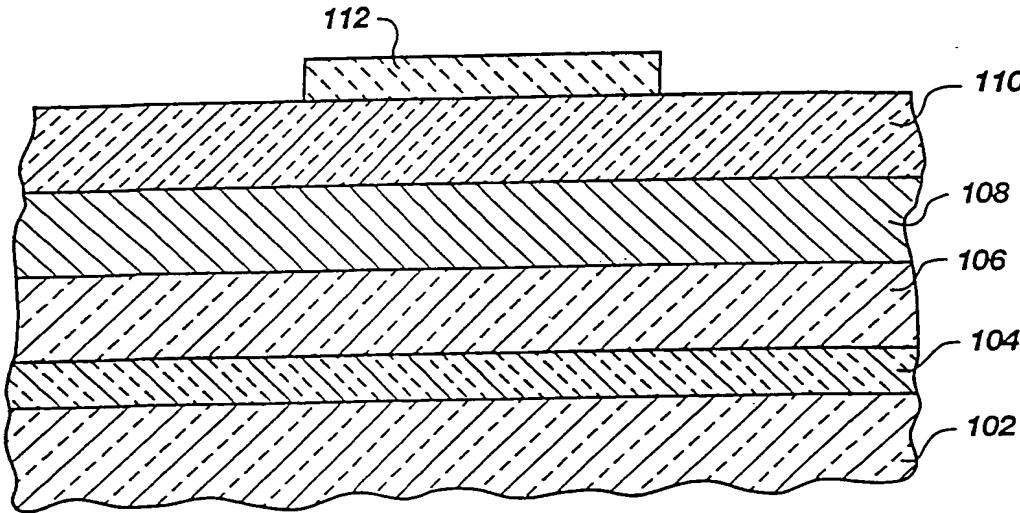


Fig. 5

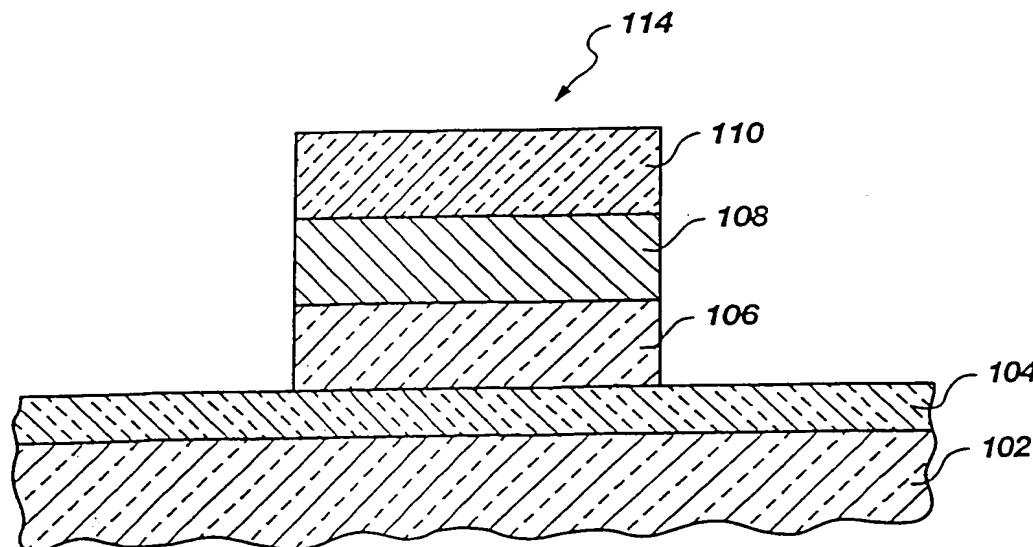
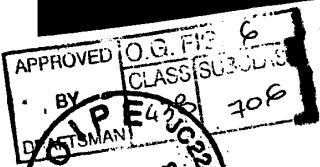


Fig. 6

(24)



TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
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Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

4/14

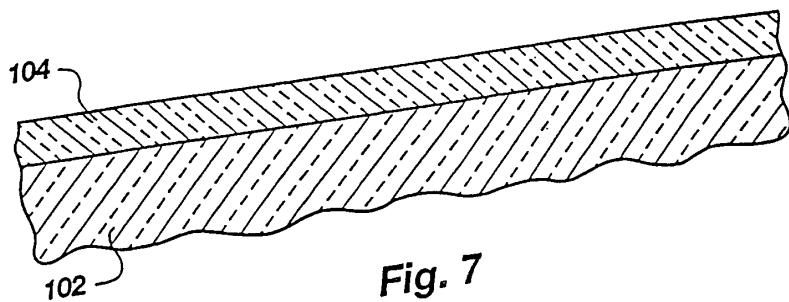


Fig. 7

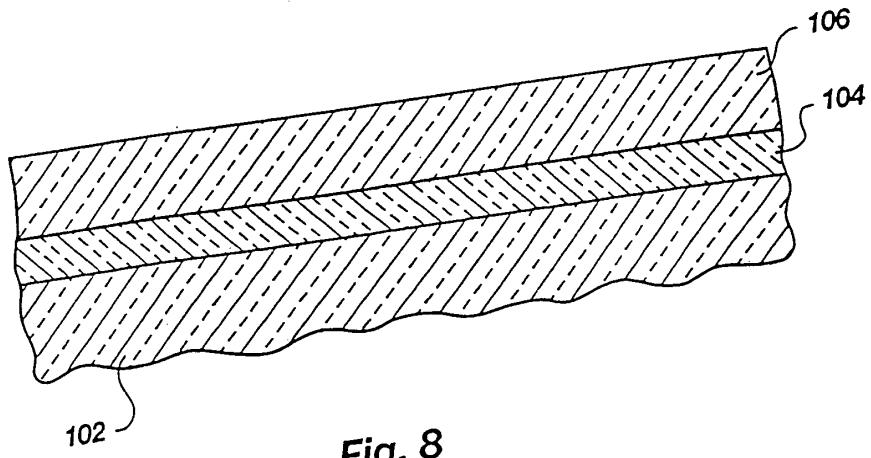


Fig. 8

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O.G. FIG. 6	CLASS 438	SUBCLASS 708
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Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US



5/14

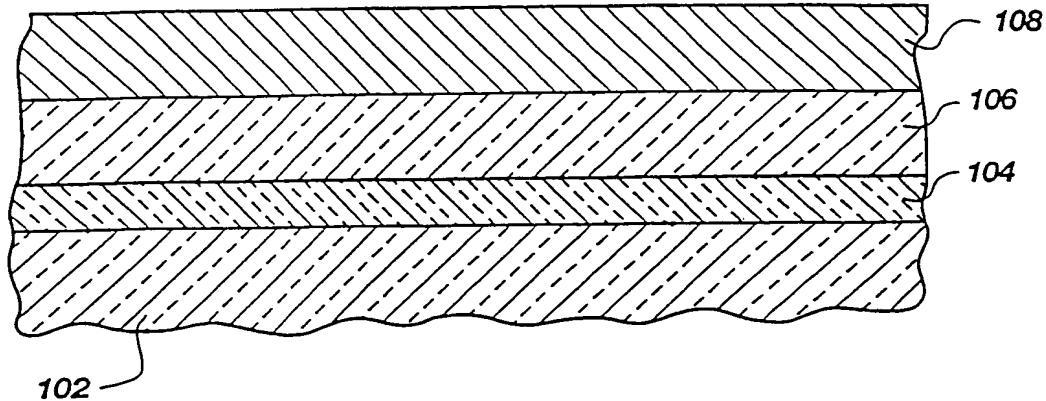


Fig. 9

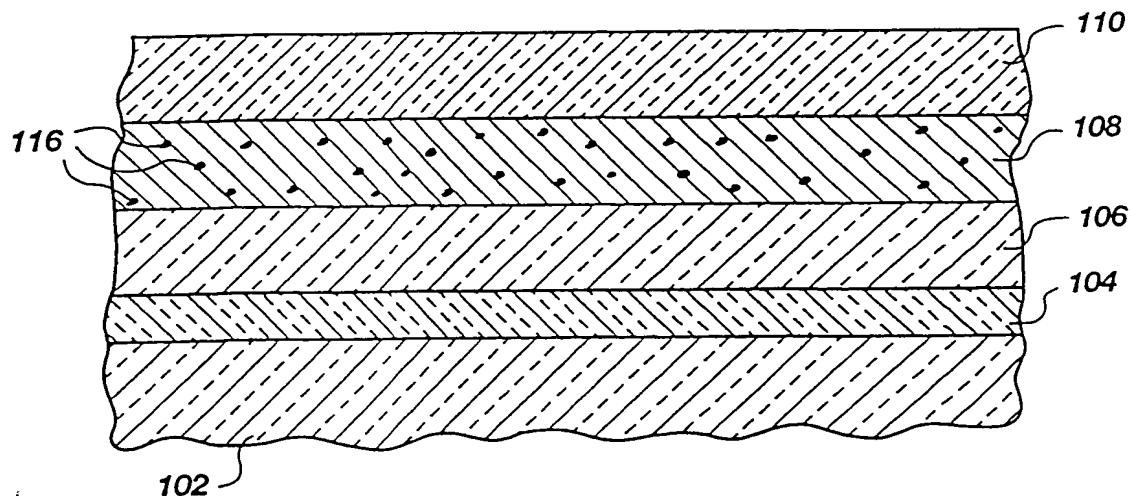


Fig. 10

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O.G. FIG. 6
CLASS SUBCL. SS
438 706

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Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

6/14

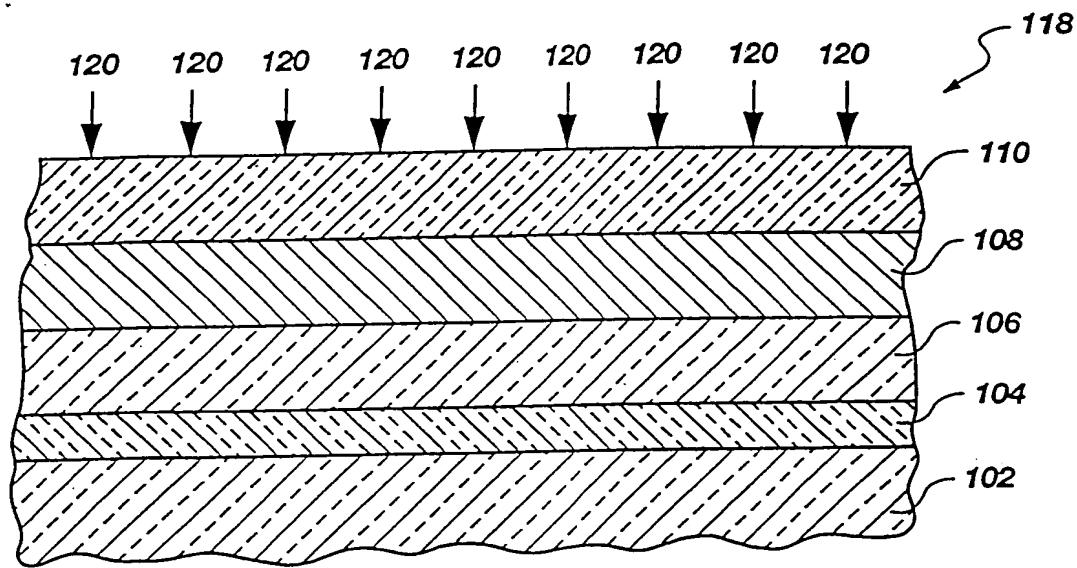


Fig. 11

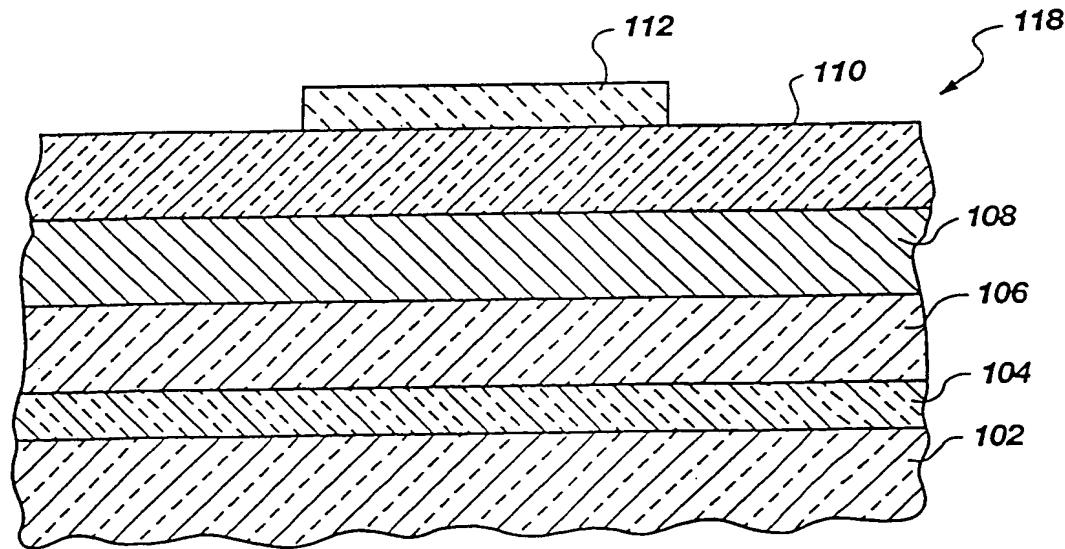
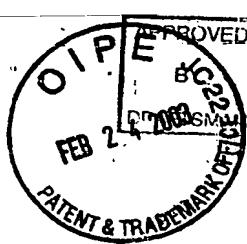


Fig. 12



APPROVED O.G. FIG. TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
CLASS SUBS

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SUBSTRATE DURING GATE STACK LATCH

Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

7/14

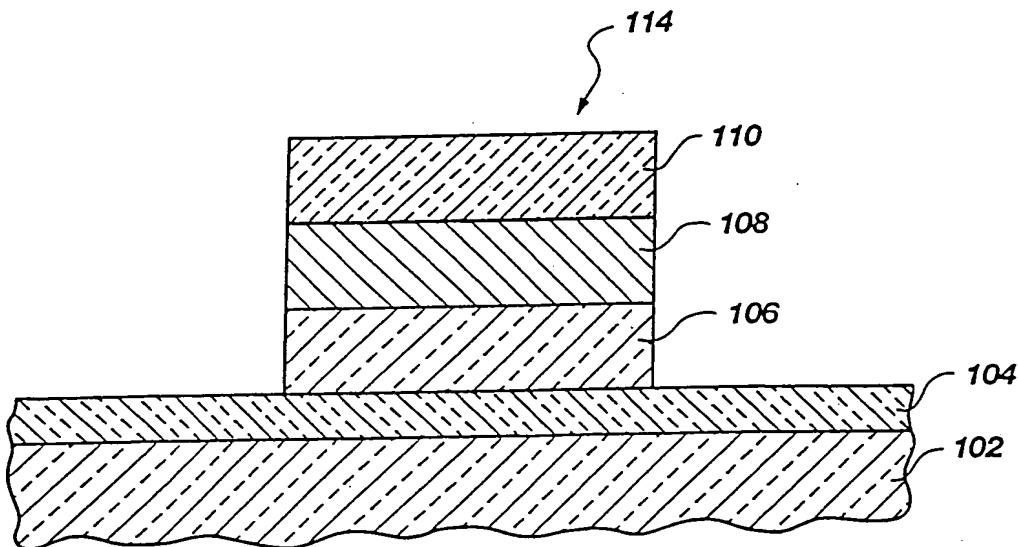


Fig. 13

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O.G. P.I.B. 6
CLASS 1 SUBCL 1
438 706

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Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

8/14

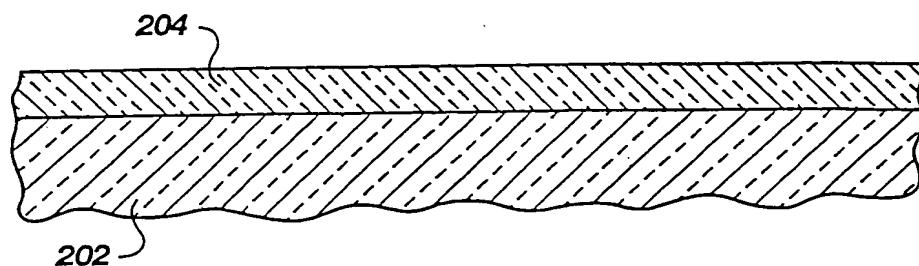


Fig. 14
(PRIOR ART)

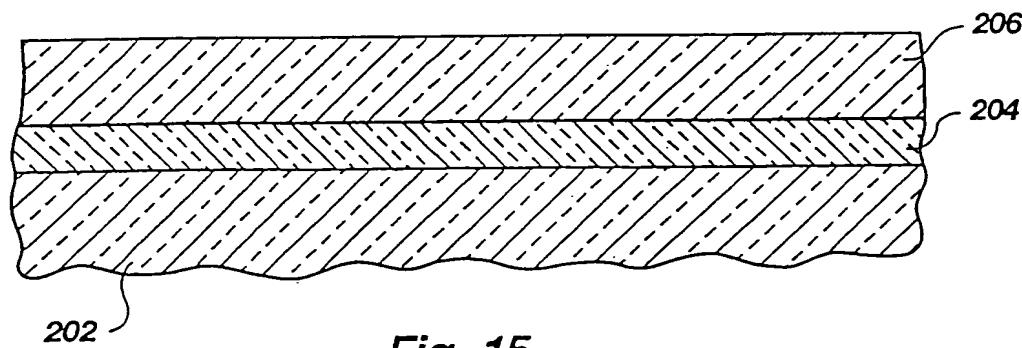


Fig. 15
(PRIOR ART)

APPROVED

O.G. FIG. 6

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TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
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Inventor: Pan et al.

Serial No.: 09/614,113

Docket No.: 2269-2915.3US

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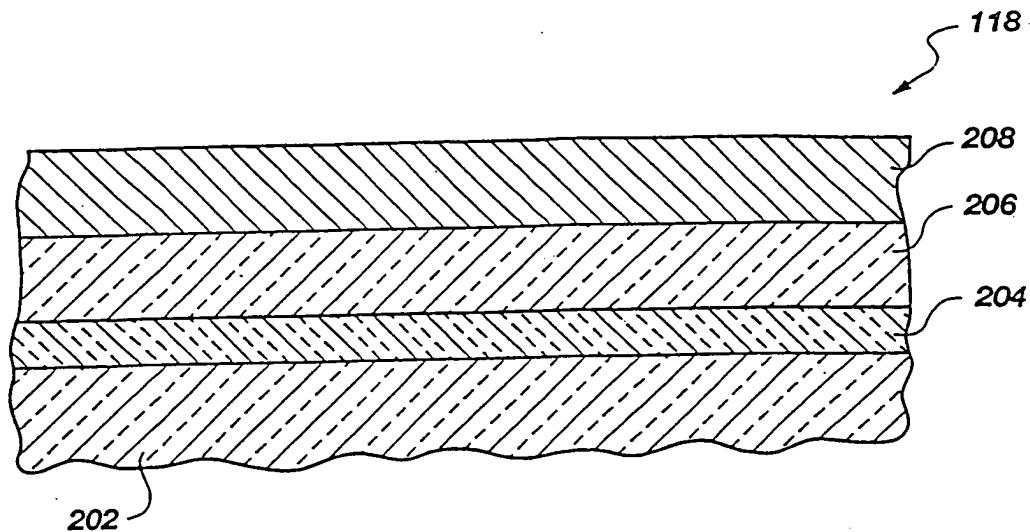


Fig. 16
(PRIOR ART)

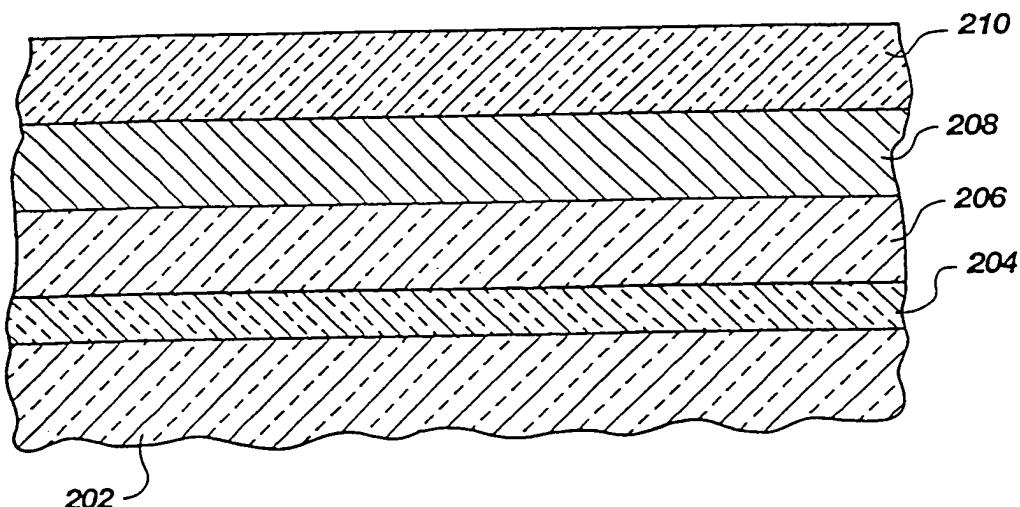
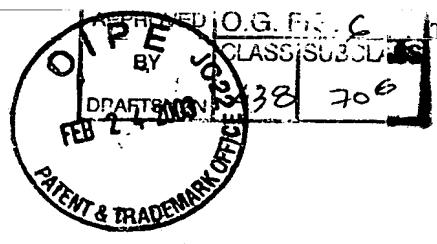


Fig. 17
(PRIOR ART)



REFINED O.G. FIG. 6
BY CLASS/SUBCLASS
TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
SUBSTRATE DURING GATE STACK ETCHING
Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

10/14

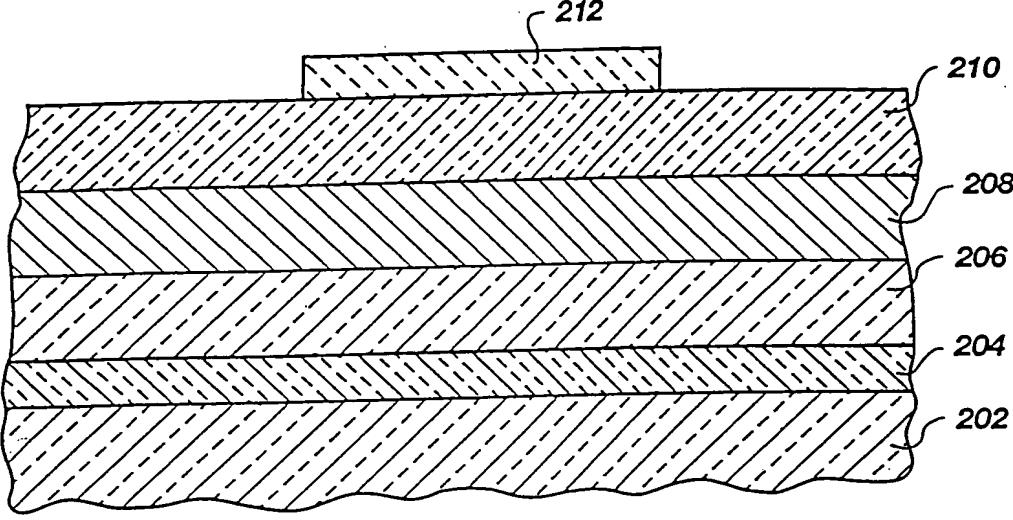


Fig. 18
(PRIOR ART)

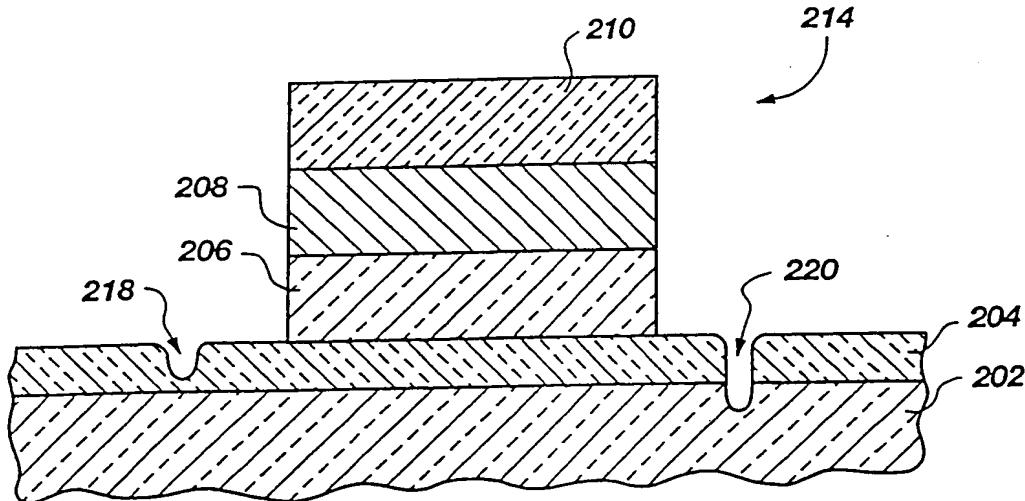
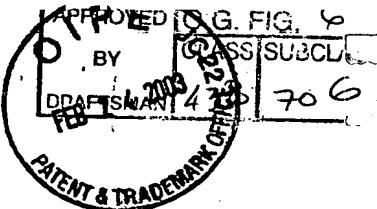


Fig. 19
(PRIOR ART)



TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
SUBSTRATE DURING GATE STACK ETCH

Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

11/14

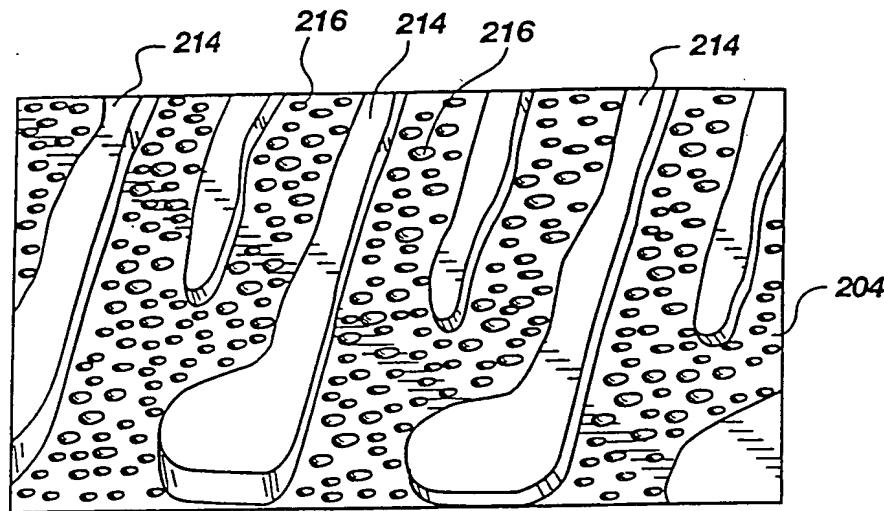


Fig. 20
(PRIOR ART)

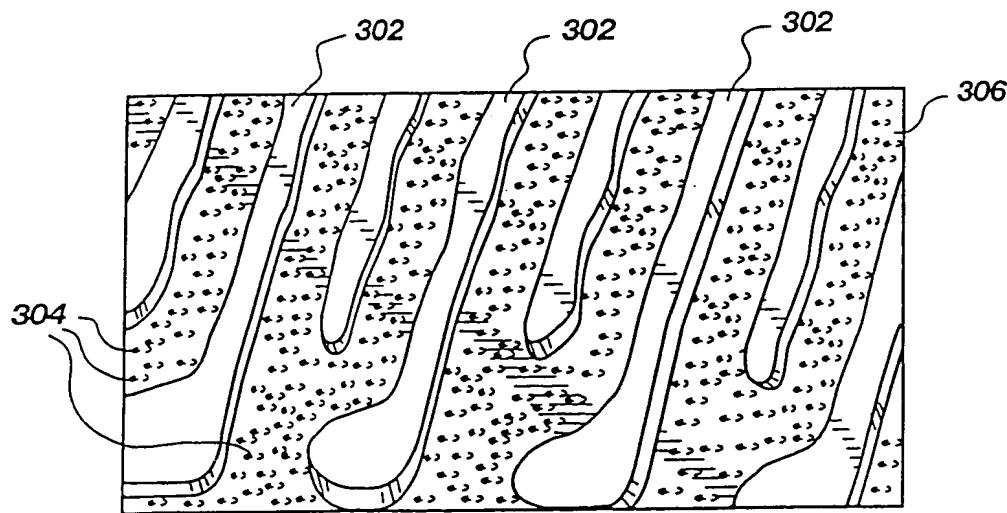


Fig. 21
(PRIOR ART)

APPROVED O.G. FIG. 8
BY CLASS SUBCLAS
DRAFTED 430 706

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SUBSTRATE DURING GATE STACK ETC.

Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

12/14

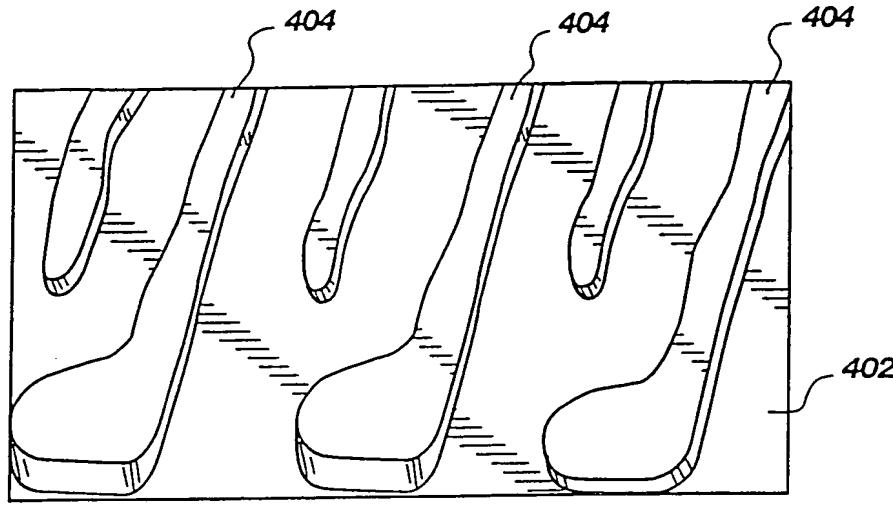
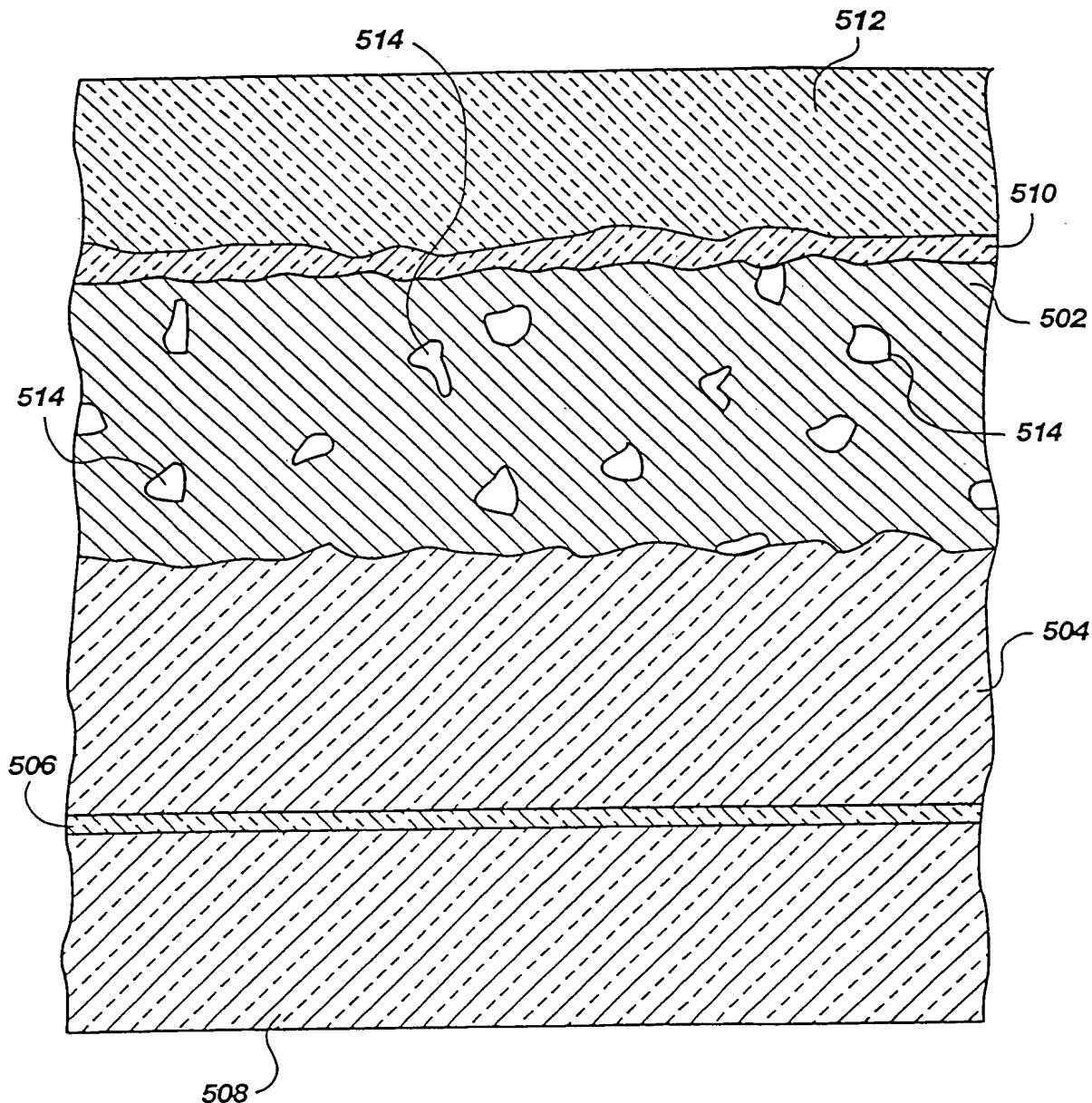


Fig. 22

APPROVED O.G. FIG. 6
BY CLASS SUBCLASS
DRAFTSMAN 438 706

TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
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Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

13/14



*Fig. 23
(PRIOR ART)*

APPROVED O.G. FIG. 6
BY 428 7-06
DRAFT MAPLE

TITLE: TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON
SUBSTRATE DURING GATE STACK ETC

Inventor: Pan et al.
Serial No.: 09/614,113
Docket No.: 2269-2915.3US

14/14

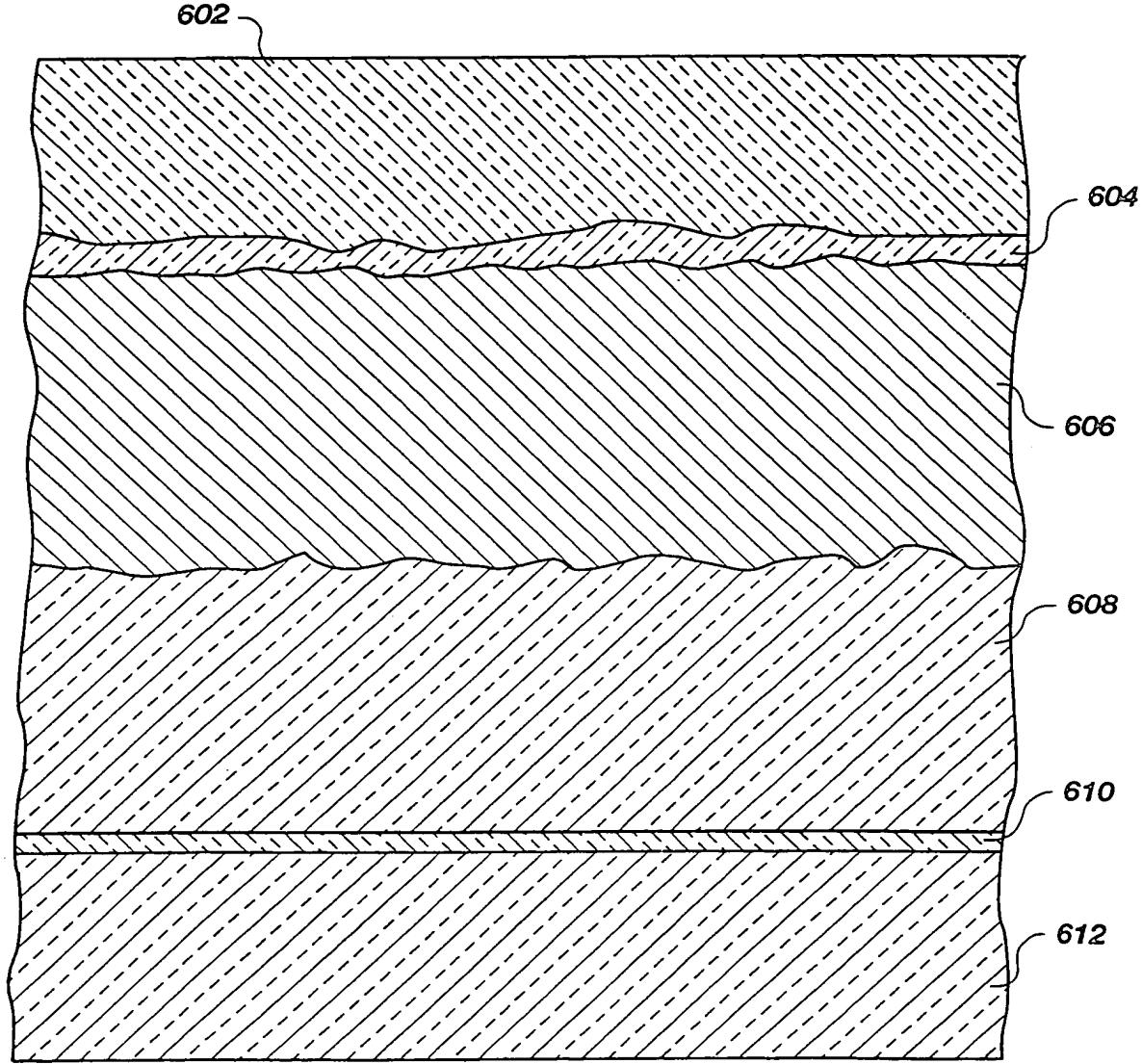


Fig. 24